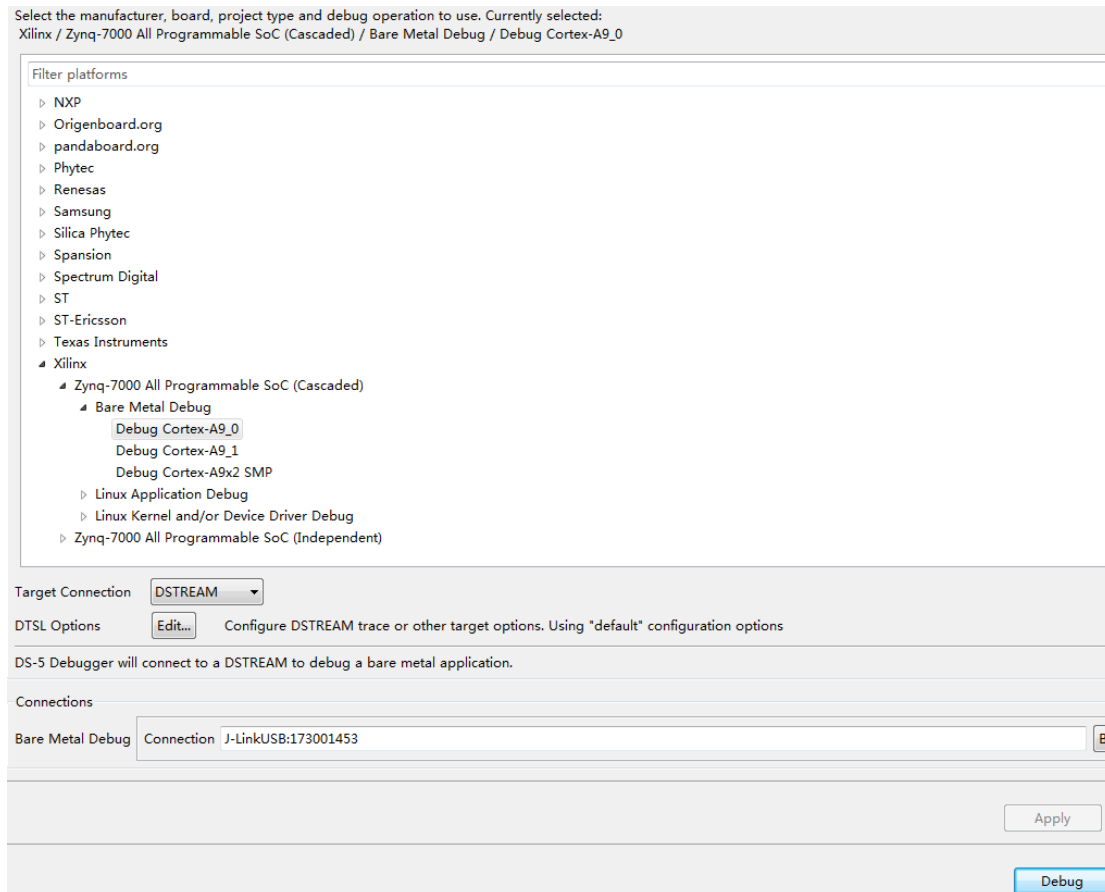
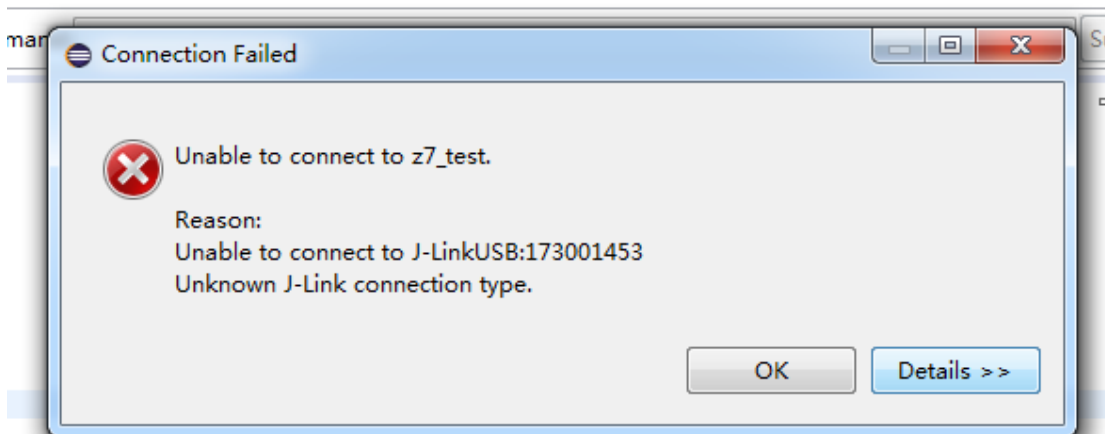




after press "debug" button, DS-5 shows



When using JLINK configuration,

```
JLink V5.10f
ROMTbl 0 [2]: 00003003, CID: B105900D, PID:04-004BB912 TPIU
ROMTbl 0 [3]: 00004003, CID: B105900D, PID:04-001BB908 CSTF
ROMTbl 0 [4]: 00005003, CID: B105900D, PID:04-002BB913 ITM
ROMTbl 0 [5]: 00009003, CID: B105900D, PID:04-002BB906 ECT / CTI
ROMTbl 0 [6]: 0000A003, CID: B105900D, PID:04-002BB906 ECT / CTI
ROMTbl 0 [7]: 0000B003, CID: B105900D, PID:00-000C9001
ROMTbl 0 [8]: 0000C003, CID: B105900D, PID:03-072893B2
ROMTbl 0 [9]: 00080003, CID: B105100D, PID:04-000BB4A9 ROM Table
ROMTbl 1 [0]: 00010003, CID: B105900D, PID:04-000BBC09 Cortex-A9
Found Cortex-A9 r3p0
6 code breakpoints, 4 data breakpoints
Debug architecture ARMv7.0
Data endian: little
Main ID register: 0x413FC090
I-Cache L1: 32 KB, 256 Sets, 32 Bytes/Line, 4-Way
D-Cache L1: 32 KB, 256 Sets, 32 Bytes/Line, 4-Way
System control register:
  Instruction endian: little
  Level-1 instruction cache disabled
  Level-1 data cache disabled
  MMU disabled
  Branch prediction disabled
Found 2 JTAG devices, Total IRLen = 10:
#0 Id: 0x23727093, IRLen: 06, Unknown device
#1 Id: 0x4BA00477, IRLen: 04, IRPrint: 0x1, CoreSight JTAG-DP (ARM)
```