SEGGER J-Flash ARM V4.40b - [C:\ProgramData\Roche\MeCIS\data\dat-files\9180_uC-Files\LPC2292.jflash]

File Edit View Target Options Window Help

Project - LPC2292

Name	Value
Connection	USB [Device 0]
Target interface	JTAG
Init JTAG speed	Auto recognition
JTAG speed	Auto recognition
TAP number	<not used>
IRPre	<not used>
MCU	NXP LPC2292
Clock speed	60000000 Hz
Endian	Little
Check core Id	Yes
Core Id	0x4F1F0F0F
Use target RAM	Yes
RAM address	0x40000000
RAM size	16 KB
Flash memory	LPC2292 internal
Manufacturer	NXP
Size	248 KB
Flash Id	0x0
Check flash Id	No
Base address	0x0
Organization	32 bits x 1 chip

C:\ProgramData\Roche\MeCIS\data\dat-files\9180_uC-Files\pack_handler.hex

Address: 0x0 x1 x2 x4

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
0000	18	F0	9F	E5	18	F0	9F	E5	18	F0	9F	E5	18	F0	9F	E5	.....
0010	18	F0	9F	E5	60	6F	A0	B8	14	F0	9F	E5	14	F0	9F	E5	....`o.....
0020	3C	00	00	00	A8	00	00	00	AC	00	00	00	B0	00	00	00	<.....
0030	B4	00	00	00	B4	92	00	00	B8	00	00	00	7C	00	9F	E5	.....¦...
0040	25	10	A0	E3	84	10	80	E5	01	10	A0	E3	80	10	80	E5	%.....
0050	AA	10	A0	E3	8C	10	80	E5	55	10	A0	E3	8C	10	80	E5	.....U.....
0060	88	10	90	E5	01	0B	11	E3	FC	FF	FF	0A	03	10	A0	E3	.....
0070	80	10	80	E5	AA	10	A0	E3	8C	10	80	E5	55	10	A0	E3	.....U...
0080	8C	10	80	E5	00	10	A0	E3	00	10	80	E5	03	10	A0	E3	.....
0090	04	10	80	E5	02	10	A0	E3	00	10	80	E5	00	10	A0	E3	.....
00A0	00	11	80	E5	09	00	00	EA	FE	FF	FF	EA	FE	FF	FF	EA	.....
00B0	FE	FF	FF	EA	FE	FF	FF	EA	FE	FF	FF	EA	7C	24	00	EA	.....¦$..
00C0	00	C0	1F	E0	00	00	00	00	00	00	00	00	00	00	00	00	.....
00D0	00	00	0F	E1	1F	00	C0	E3	1B	10	80	E3	01	F0	2F	E1	...../.
00E0	64	D1	9F	E5	17	10	80	E3	01	F0	2F	E1	5C	D1	9F	E5	d...../.\...
00F0	12	10	80	E3	01	F0	2F	E1	54	D1	9F	E5	11	10	80	E3	...../.T.....
0100	01	F0	2F	E1	4C	D1	9F	E5	13	10	80	E3	01	F0	2F	E1	../.L...../.
0110	44	D1	9F	E5	1F	10	80	E3	01	F0	2F	E1	3C	D1	9F	E5	D...../.<...
0120	3C	01	9F	E5	3C	11	9F	E5	3C	21	9F	E5	39	00	00	EB	<...<...<!..9...
0130	38	01	9F	E5	38	11	9F	E5	38	21	9F	E5	35	00	00	EB	8...8...8!..5...
0140	34	01	9F	E5	34	11	9F	E5	34	21	9F	E5	31	00	00	EB	4...4...4!..1...
0150	30	01	9F	E5	30	11	9F	E5	30	21	9F	E5	2D	00	00	EB	0...0...0!..-...
0160	2C	01	9F	E5	2C	11	9F	E5	2C	21	9F	E5	29	00	00	EB	,...,...,!..)...
0170	28	01	9F	E5	28	11	9F	E5	28	21	9F	E5	25	00	00	EB	<...<...<!..%...
0180	24	01	9F	E5	24	11	9F	E5	00	20	A0	E3	20	00	00	EB	$...$... ...

LOG

```
- J-Flash ARM V4.40b (J-Flash compiled Dec 22 2011 10:55:27)
- JLinkARM.dll V4.40b (DLL compiled Dec 22 2011 10:54:45)
Reading flash device list [C:\Programme\SEGGER\JLinkARM_V440b\ETC\JFlash\Flash.csv] ...
 - List of flash devices read successfully (1028 Devices)
Reading MCU device list [C:\Programme\SEGGER\JLinkARM_V440b\ETC\JFlash\MCU.csv] ...
 - List of MCU devices read successfully (1058 Devices)
Opening project file [C:\ProgramData\Roche\MeCIS\data\dat-files\9180_uC-Files\LPC2292.jflash] ...
 - Project opened successfully
Opening data file [C:\ProgramData\Roche\MeCIS\data\dat-files\9180_uC-Files\pack_handler.hex] ...
 - Data file opened successfully (50884 bytes, 1 range, CRC = 0xDFDDBCDB)
```

Ready