How to get the value of the arguments passed to the «SEGGER_OPEN_Read» function.

1. In the code, create a variable readDbg with the attribute " (section ("myDbgData")", this will allow you to find out the address of the variable in the RAM of the MCU.

```
#define PRG_DBG
#ifdef PRG_DBG
90 /*
 * mem alloc for debug infomation/
 */
typedef struct {
 U32 addr;
 U32 numBytes;
 U32 ptrDest;
} myDbgStruct_t;
100 volatile myDbgStruct_t readDbg __attribute__ ((section ("myDbgData")));
#endif
```

2. After compiling "Release", get the address of the variable "readDbg " from the .map file.

myDbgData	0x00000000 <mark>20000a74</mark>	0xc
	0x00000000 <mark>20000a74</mark>	myDbgData_start = .
(myDbgData	myDbgData.)	
myDbgData	0x00000000 <mark>20000a74</mark>	0xc Output/Release/Obj/Flashloader/FlashPrg.o
	0x000000000000000000000000000000000000	readDbg
	0x0000000020000a80	myDbgData_end = (myDbgData_start + SIZEOF (myDbgData))
	0x00000000000000c	myDbgData size = SIZEOF (myDbgData)

3. Running J-Flash. I disable the partitions associated with the internal flash memory of the microcontroller, this will allow the "read back Selected sections" command to be used.

File	Edit Target Optio	ons View Help											
Project	information	8 ×	<	General	Fli	sh bank: Bank 0 (Internal flash) 🔻	Rename		+		
Settin	g	Value		Target Interface ✓ MCU		\sim					-		
[-] Ge	eneral			Init. steps		lash bank settings							
	Project name			Exit steps		ase Address 080	00000						
	Host connection	USB [Device 0]		Flash		Organization 32 B	Bits v x	1 Chip 🔻					
[-] тэ	(F			Production		-		T Olip					
	Туре	SWD		Performance		Disable flash ba	nk						
	Init. speed	4000 kHz				lash device info							
	Speed	4000 kHz				lasit device into				_			
[-] Ta	arget					Info Inte	rnal flash bar	nk 0 (Static secto	r layout)				
	MCU	ST_STM32F207VC				Anufacturer ST							
	Core	Cortex-M3											
	Endian	Little				Device STM	132F207VC						
	Check core ID	Yes (0x4BA00477)				Size	256 KB	Sectors		6			
	Use target RAM	120 KB @ 0x20000000						L		_			
[-] F]	lashbank No. 0					ector selection							
	Туре	Internal bank 0					_						
	Base address	0x8000000				Sector	Range						
	Flash size	256 KB					000 - 0x08						
[+] F]	lashbank No. 1					<u> </u>	1000 - 0x08						
[+] F]	lashbank No. 2						000 - 0x08						
							000 - 0x08						
							9000 - 0x080 9000 - 0x080						

Project	information	8 ×	General Target Interface Flash bank: Bank 1 (OT	P flash) ▼ Rename
Settin	,	Value	Target Interface ✓ MCU Flash bank settings	Kename
-] Ge	eneral		Init. steps	
	Project name		Exit steps Base Address 1FFFC	000
_	Host connection	USB [Device 0]	Flash Organization 32 Bits	🛛 🔻 🗴 1 Chip 💌
-] TI	(F		Production	
	Туре	SWD	Performance Disable flash bank	
	Init. speed	4000 kHz	Flash device info	
	Speed	4000 kHz		
-] Ta	arget		Info Interna	al flash bank 1 (Static sector layout)
	MCU	ST_STM32F207VC	Manufacturer ST	
	Core	Cortex-M3		
	Endian	Little	Device STM32	F207VC
	Check core ID	Yes (0x4BA00477)	Size 16	Bytes Sectors 1
	Use target RAM	120 KB @ 0x20000000		
-] FI	ashbank No. 0		Sector selection	
	Туре	Internal bank 0		
	Base address	0x8000000		Range
	Flash size	256 KB	0 0x1FFFC00	00 - 0X1FFFC00F
+] F1	ashbank No. 1			
+] F1	ashbank No. 2			

4. Run command "read back Selected sections"

SEGGER J-Flash V6.94b	- [*]		_	
File Edit Target Opti	ons View Help			
Project information	8 ×			
Setting	Value			
[-] General				
Project name				
Host connection	USB [Device 0]			
-] TIF				
Туре	SWD			
Init. speed	4000 kHz			
Speed	4000 kHz			
[-] Target				
MCU	ST_STM32F207VC			
Core	Cortex-M3			
Endian	Little	Drag & Drap data filo hara		
Check core ID	Yes (0x4BA00477)	Drag & Drop data file here		
Use target RAM	120 KB @ 0x20000000	J-Flash ? X		
		Range 1/1, 0x0 - 0x3FFFFF 9% Cancel		
.og				
 MARNING: Flash bank 1 Start of determining fl Flash bank info: 1824 * 4 KB @ 0x00000 Start of preparing flas Blank checking 0x0000 	flash info (Bank 2 @ 0x000 ash info 000 ash programming h programming 0000 - 0x0001FFFF 0000 - 0x0001FFFF 0000 - 0x0005FFFF	ee)		
- Blank checking 0x0006	0000 - 0X000/FFFF			
Ready		Connected Core Id: 0x2BA01477 S	Speed: 400)0 kH:

J-Flash reading target memory, but print "Blank checking" in log window. It's normal?

Let's wait for the end of the operation.

J S	EGGER J-Flash V6.94b -	· [*]		_	
File	Edit Target Optio	ons View Help			
Project	information				
Settin	g	Value			
[-] Ge	eneral				
	Project name				
	Host connection	USB [Device 0]			
[-] ті	(F				
	Туре	SWD			
	Init. speed	4000 kHz			
	Speed	4000 kHz			
[-] Ta	arget				
	MCU	ST_STM32F207VC			
	Core	Cortex-M3			
	Endian	Little	Drag & Drop data file here		
	Check core ID	Yes (0x4BA00477)			
	Use target RAM ashbank No. 0	120 KB @ 0x200 🔜 J-FI;	ish V6.94b ? ×		
Log			Target memory read successfully. (4194304 bytes, 1 range) - Completed after 20.657 sec		
_	ank checking 0x00300	1000 - 0x0031FFFF			
- B1	ank checking 0x00320	0000 - 0x0033FFFF			
	ank checking 0x00340 ank checking 0x00360				
	ank checking 0x00380				
	ank checking 0x003A0 ank checking 0x003C0				
	ank checking 0x003E0				
	art of restoring				
	d of restoring rget memory read suc	ccessfully. (4194304 bytes	, 1 range) - Completed after 20.657 sec		
Ready			Connected Core Id: 0x2BA01477	Speed: 400	00 kHz

5. Run JMem. Reading variable "readDbg" used address get from .map file.

R SEGGE	R J-Mem V6.9	94b				_		\times
<u>F</u> ile <u>T</u> arg	jet <u>O</u> ptions	; <u>H</u> elp						
Go To: 200	000A74	~ (0 🗋 📩	Periodic Refresh: off 🛛 🔻			
20000A70	00000000	00238000	00000000	20001658	<mark>.</mark> .#X			
20000A80	FFFFFFFF	FFFFFFFF	FFFFFFF	FFFFFFFF	<u> 99999999999999999</u>			
20000A90	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 99999999999999999</u>			
20000AA0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 99999999999999999</u>			
20000AB0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 99999999999999999</u>			
20000AC0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 99999999999999999</u>			
20000AD0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000AE0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000AF0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B00	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B10	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B20	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B30	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B40	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B50	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 222222222222222222</u>			
20000B60	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 222222222222222222</u>			
	FFFFFFFF		FFFFFFFF	FFFFFFFF	<u> </u>			
20000B80	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000B90	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000BA0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 222222222222222222</u>			
20000BB0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> 222222222222222222</u>			
	FFFFFFFF			FFFFFFFF	<u> </u>			
20000BD0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000BE0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000BF0	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000C00		FFFFFFFF		FFFFFFFF	<u> 22222222222222222</u>			
20000C10	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000C20	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFFF	<u> </u>			
20000C30	FFFFFFFF	FFFFFFFF	FFFFFFFF	FFFFFFF	<u> </u>			
Connect to ta	arget		C	onnected	ARM core id: 0x2BA01477	Speed	: 2000 kHz	z